

Patented 06-26-00

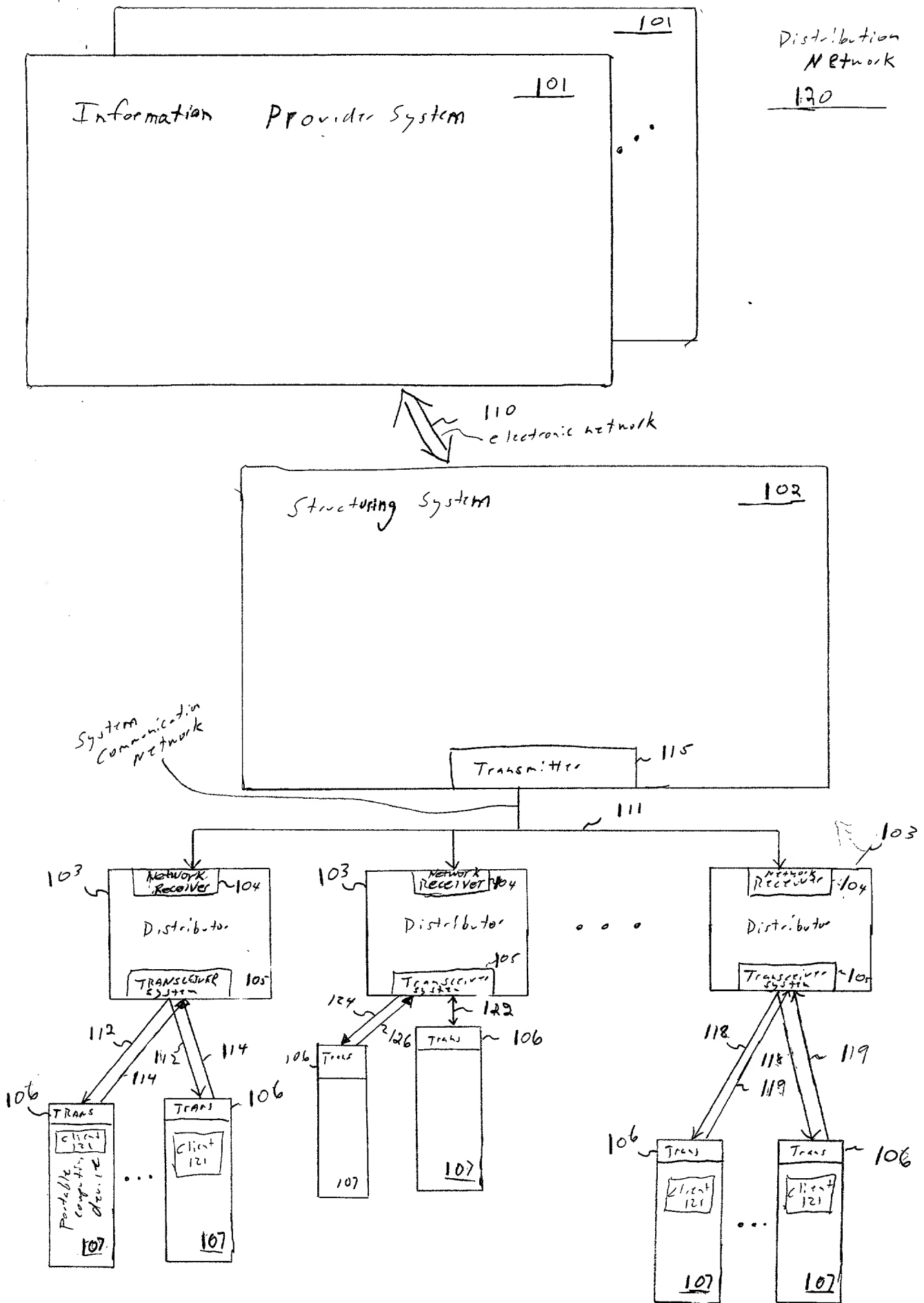


FIG. 1

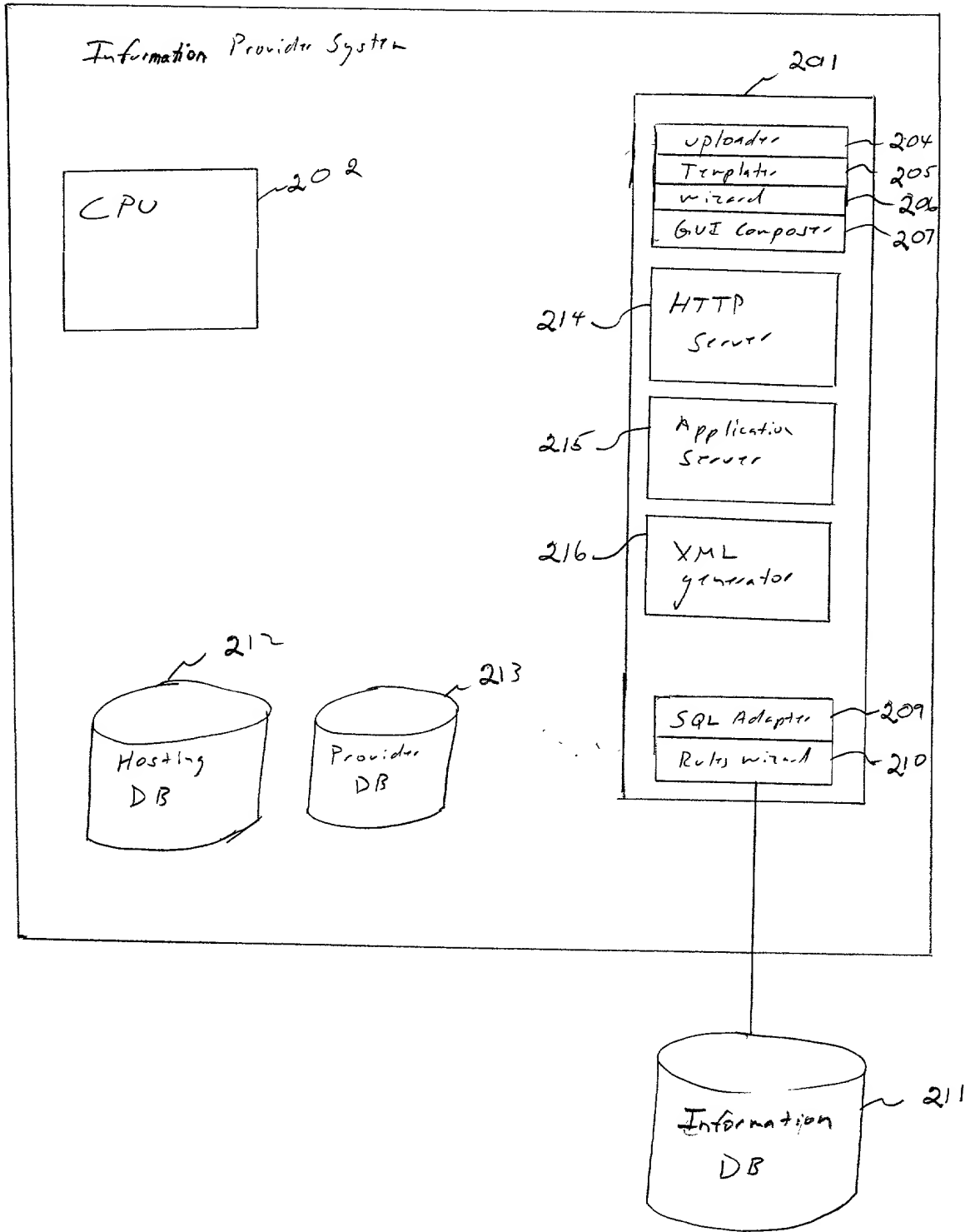
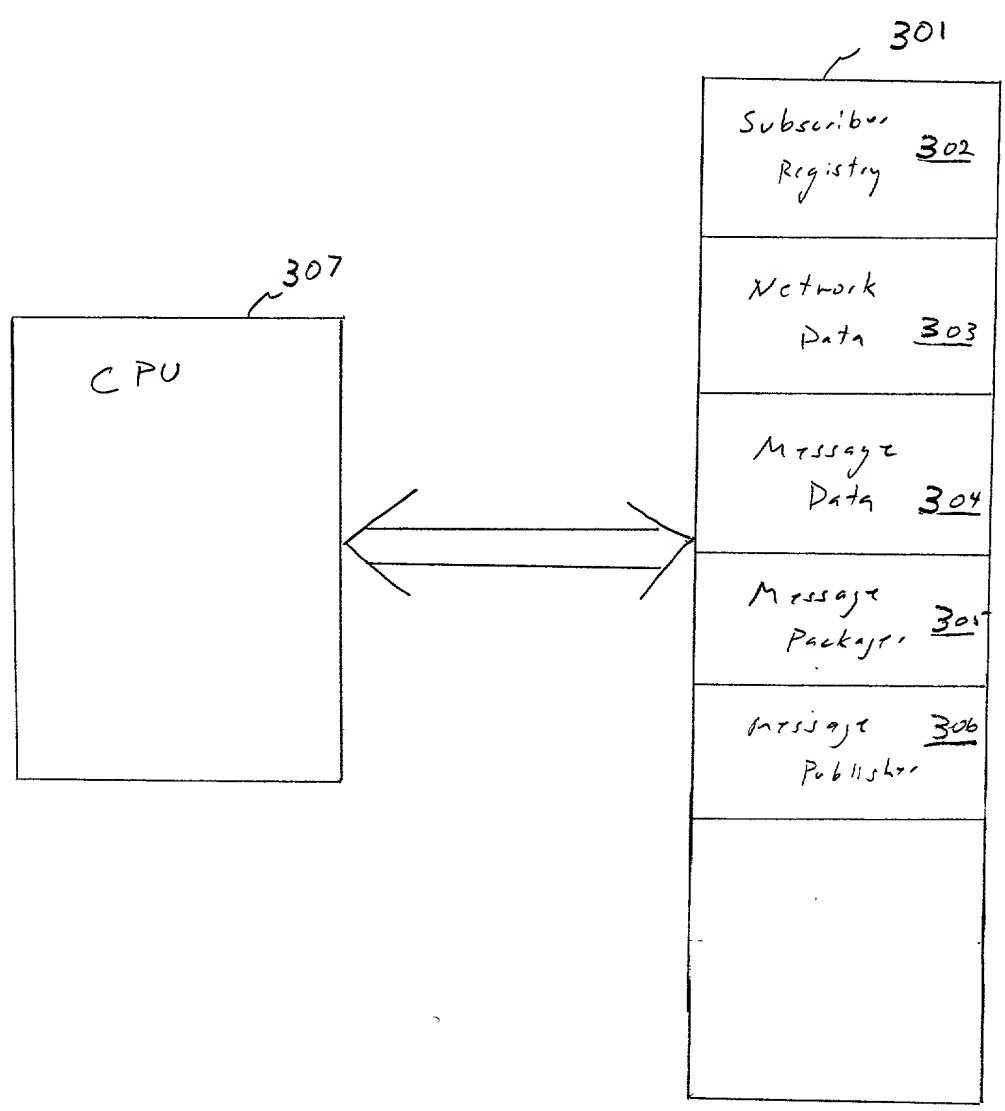


Fig. 2

Structuring System



TRANSMITTER 115

~ III

FIG. 3

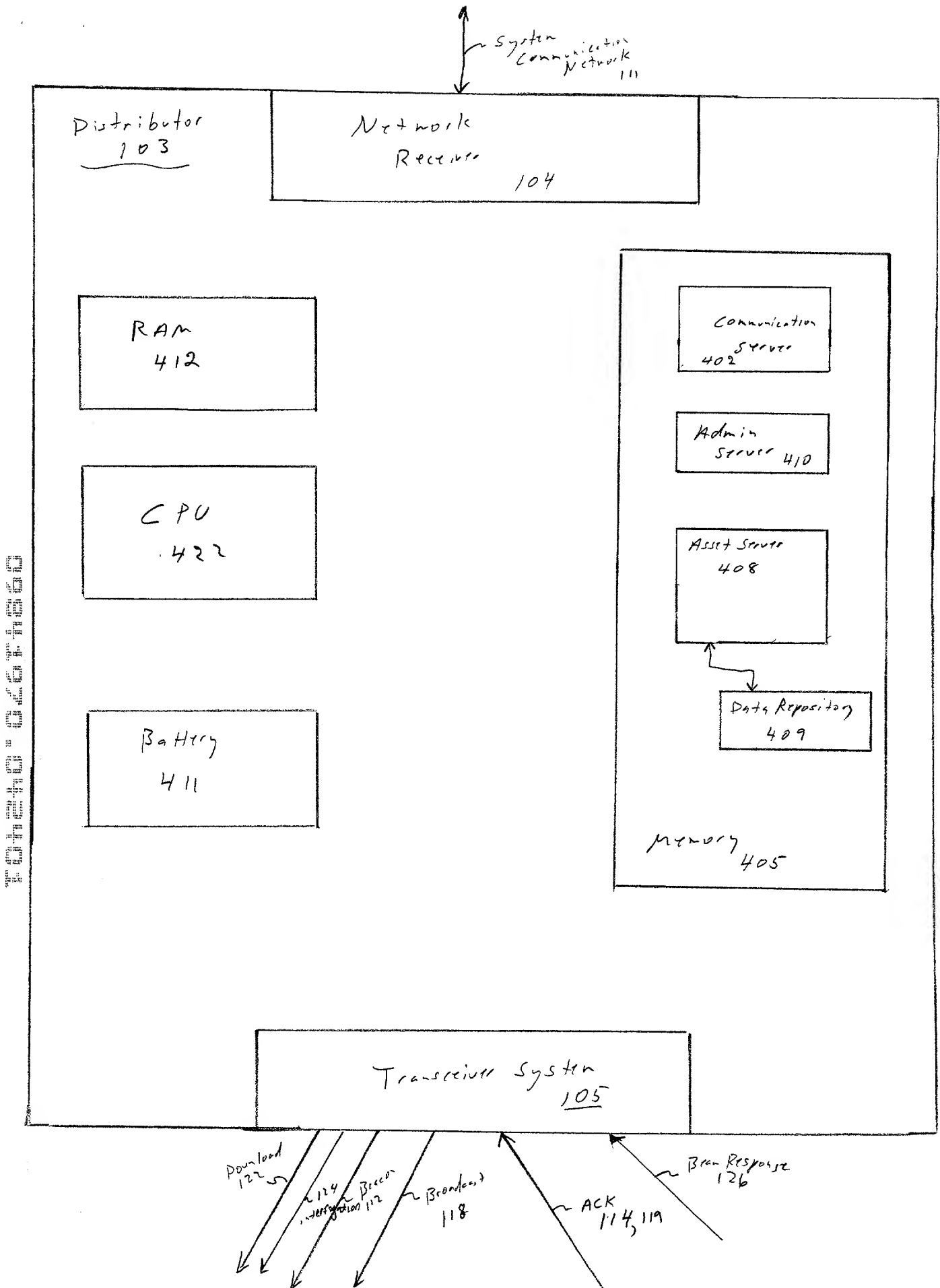


FIG. 4

transceiver system
105

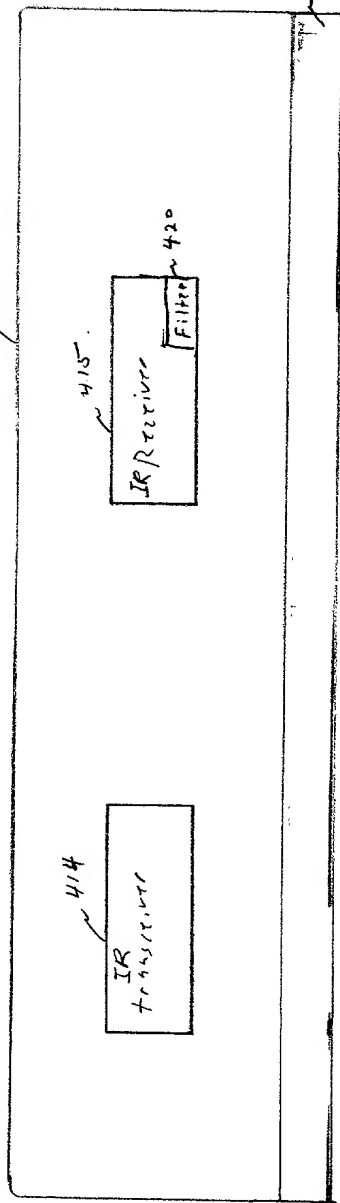


FIG. 4A

transceiver system
105

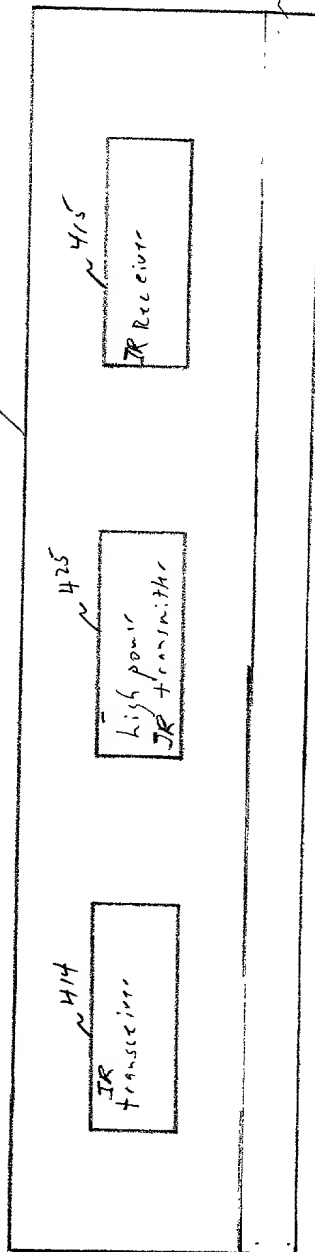


FIG. 4B

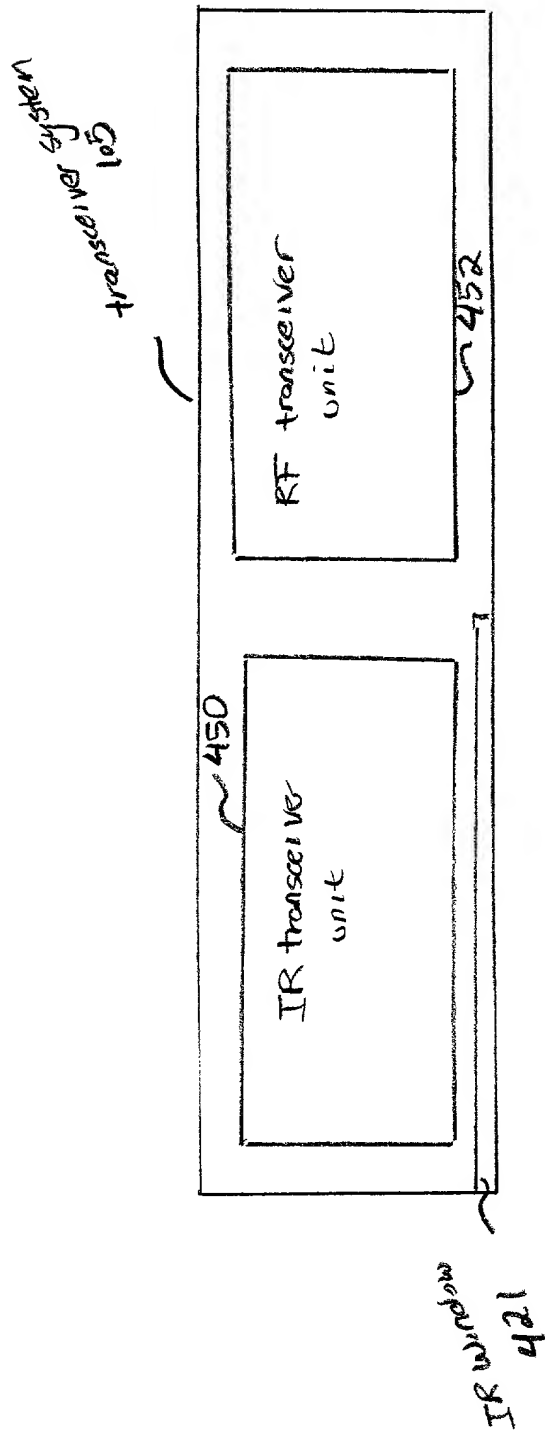


Fig. 4C

APPLICATION LAYER

COM
SERVER
402

ASSET
SERVER
408

DB
409

ADMIN
SERVER
410

PLATFORM LAYER

Cross
platform
interface

Manager

Platform
independent
interface

Platform Feature Set

Status Indicator Interface

H/W op. access

Abstract
endpoint
interface

test
function
suite

SYSTEM LAYER

H/W
drivers

Kernel
RTOS

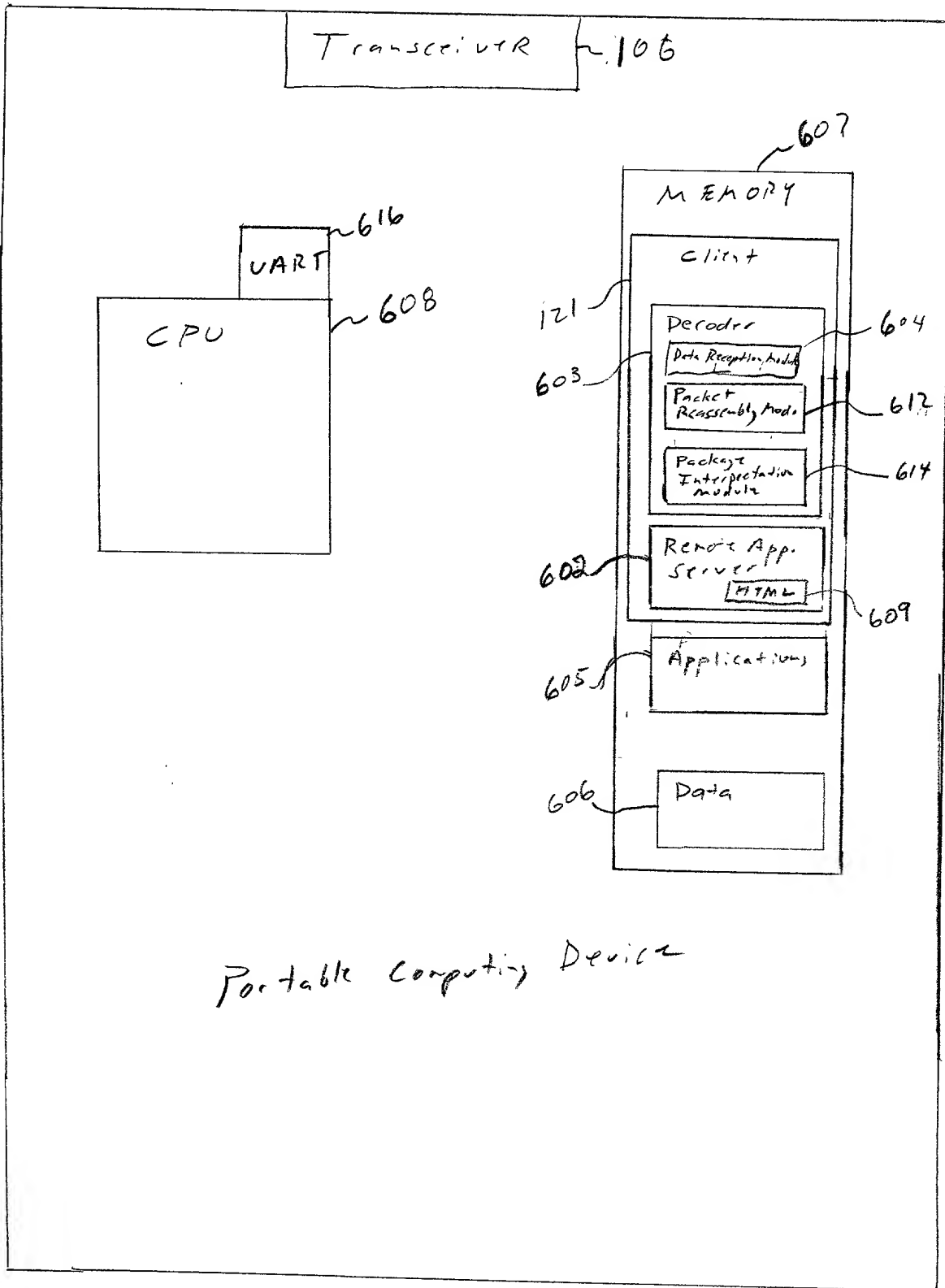
Boot Loader
520

Transceiver
Protocol
stacks

Network
Receiver
Comm
stacks

test
function
517

Fig. 5



Portable Computing Device

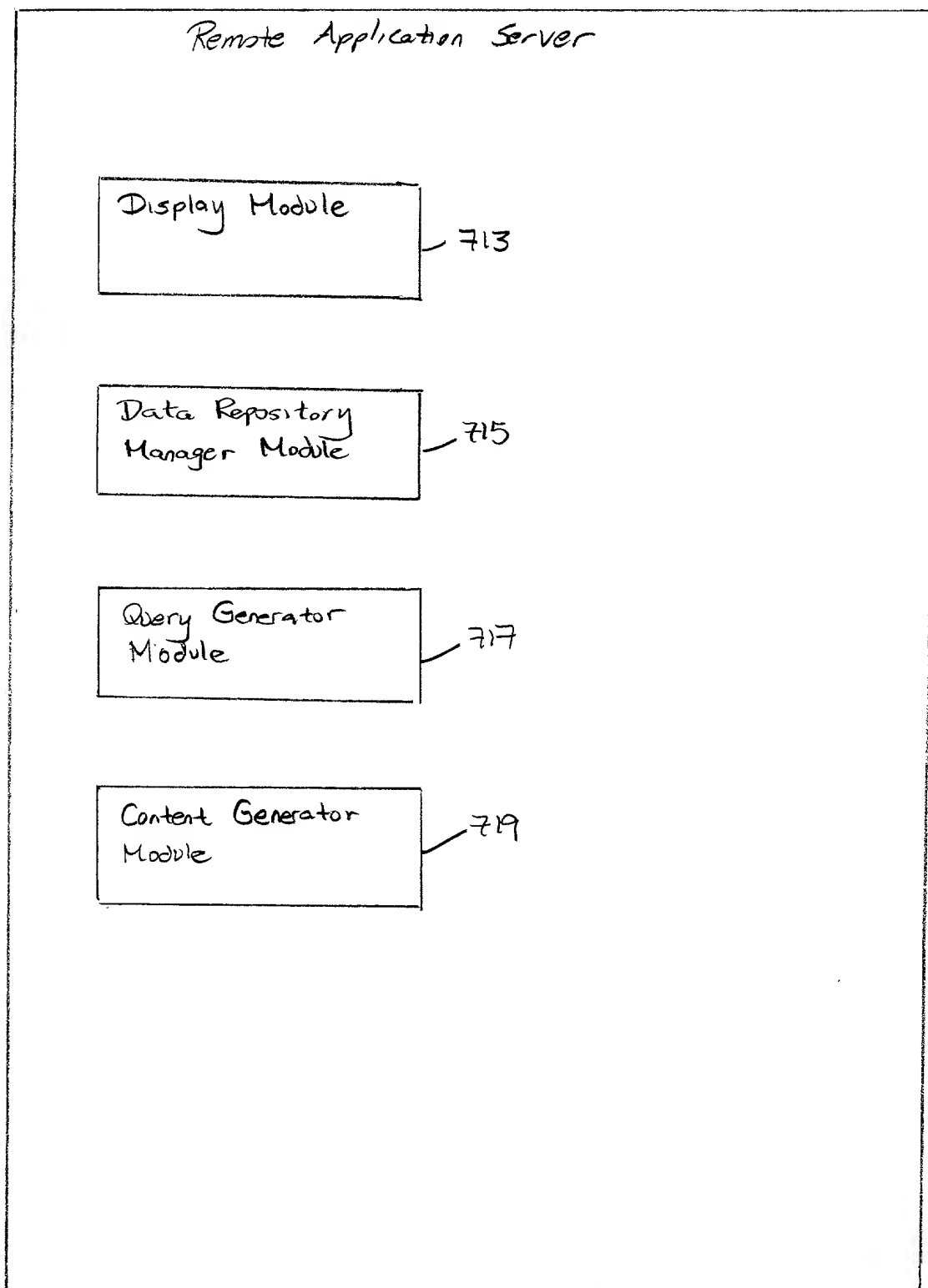


Fig. 7A

725 /

Receive Information Including Logic Portion,
Data Repository, and Formatting Information



727 /

Produce a Search Query in Accordance
with User Input and in Accordance with Logic
Portion



729 /

Search Data Repository in Accordance
with Search Query to Retrieve Search Result



731 /

Generate Content in Accordance with
Search Result and in Accordance with
Formatting Information



733 /

Display Content to user of portable
computing device

602

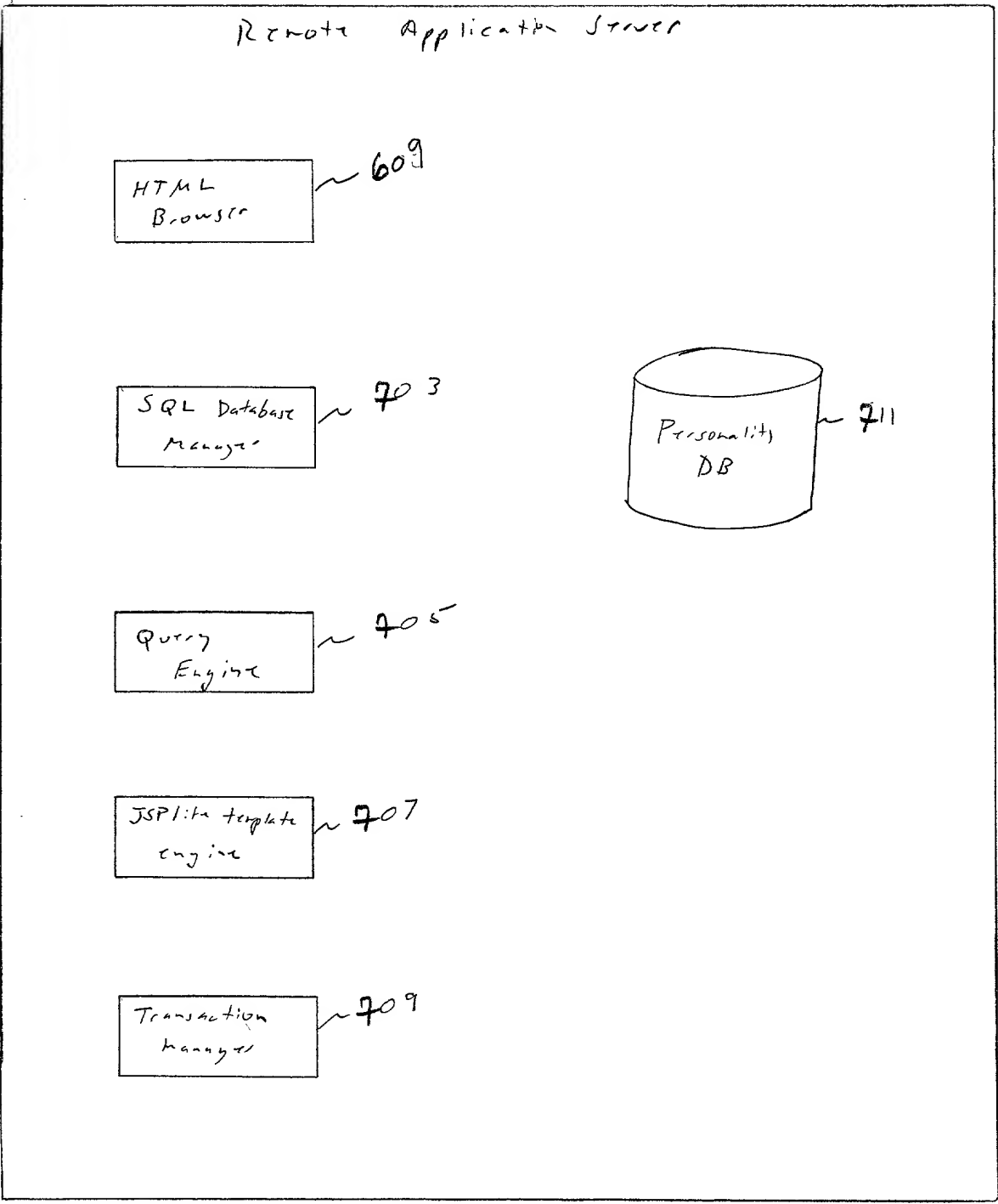


Fig. 7C

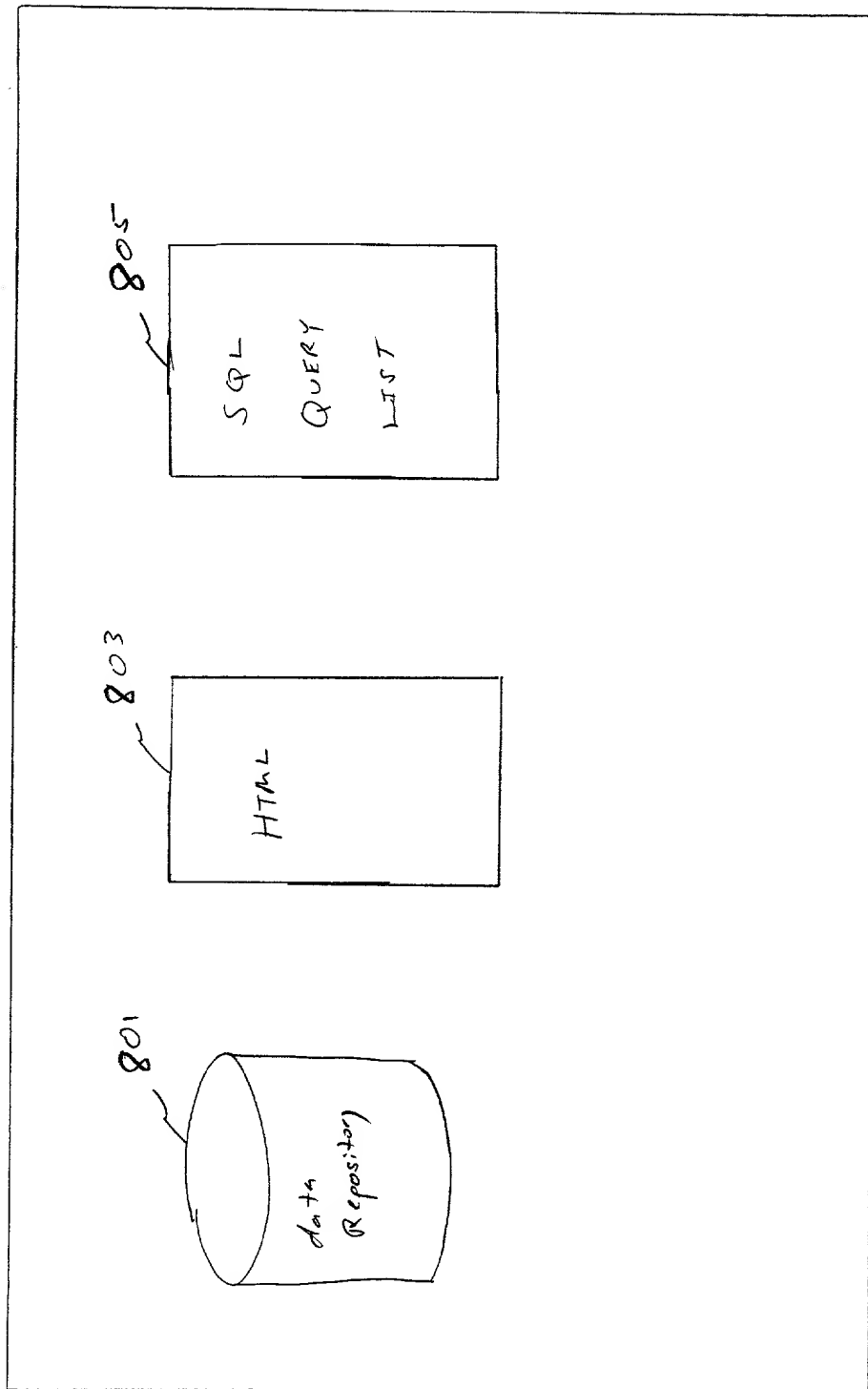


FIG. 8

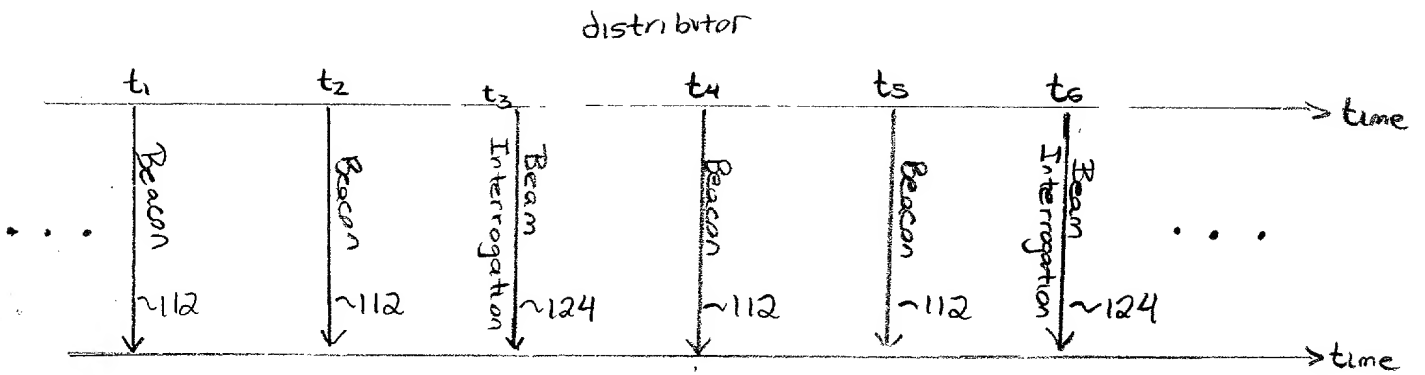


Fig. 9

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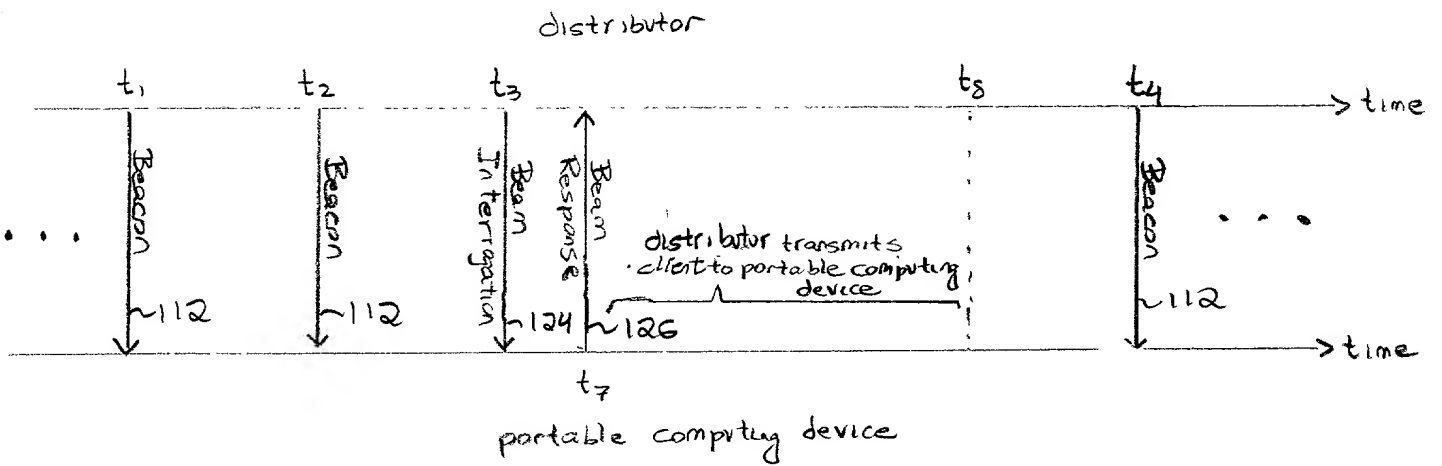
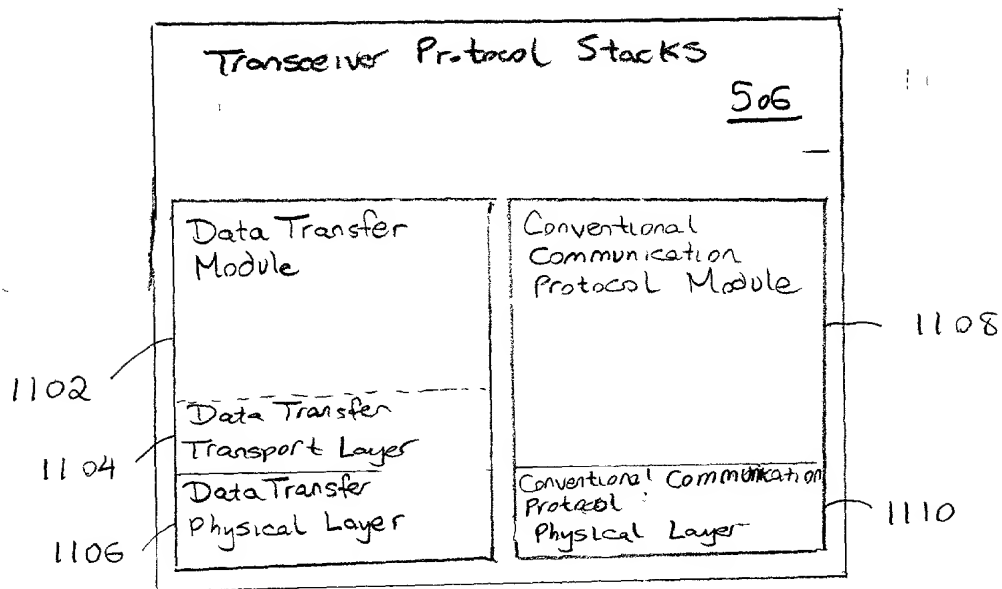
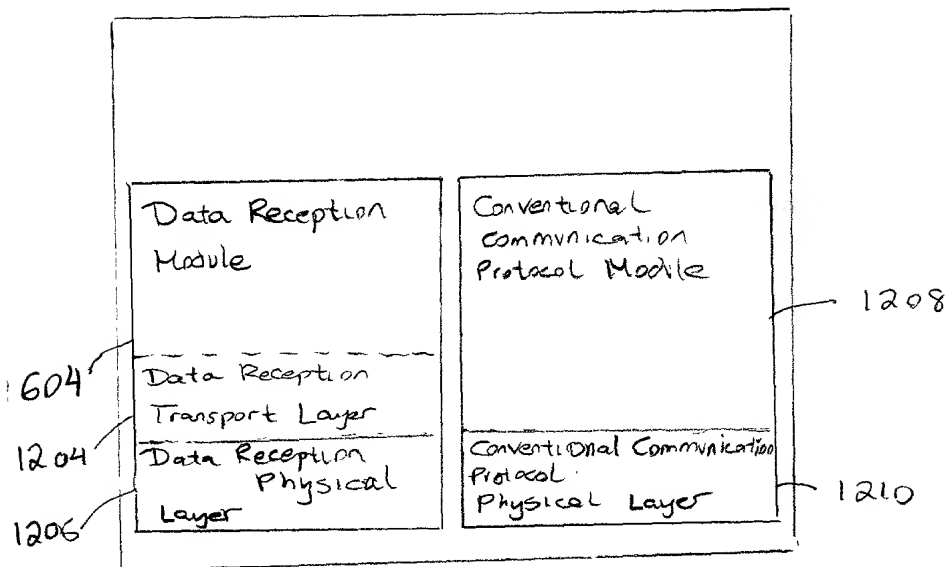


Fig. 10



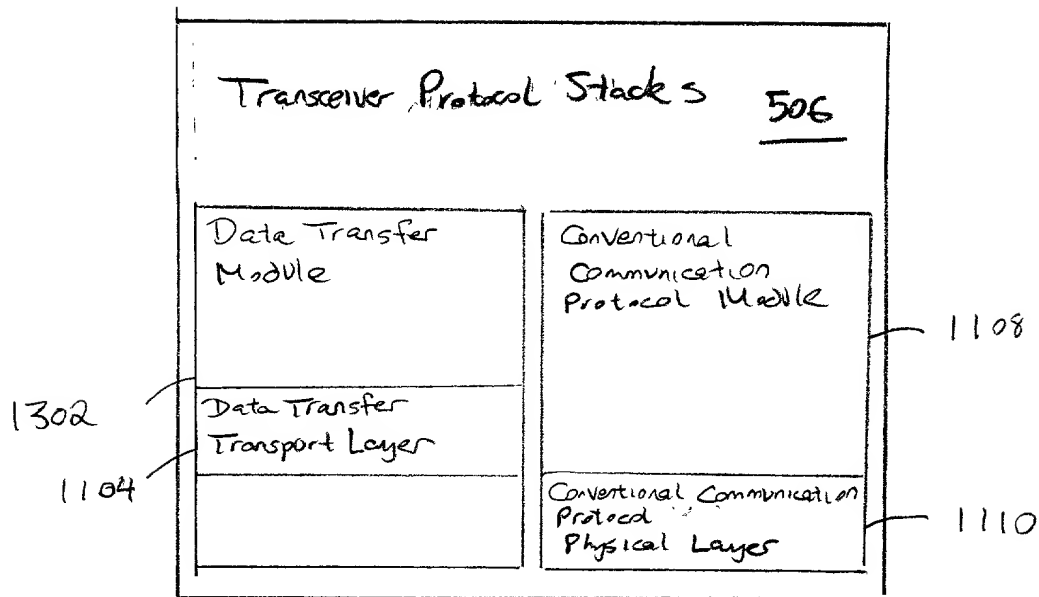
Distributor

Fig. 11



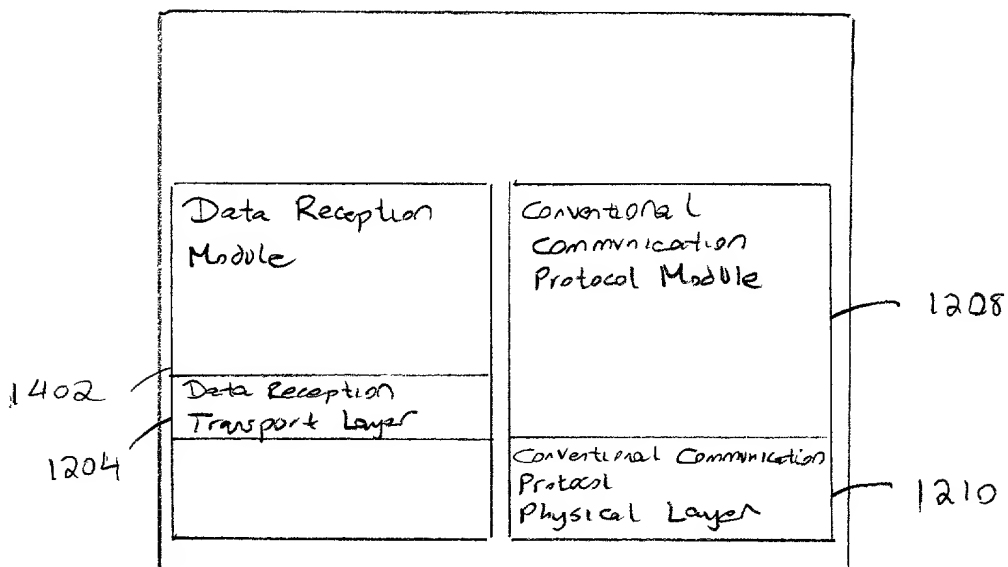
Portable Computing Device

Fig. 12



Distributor

Fig. 13



Portable Computing Device

Fig. 14

1500

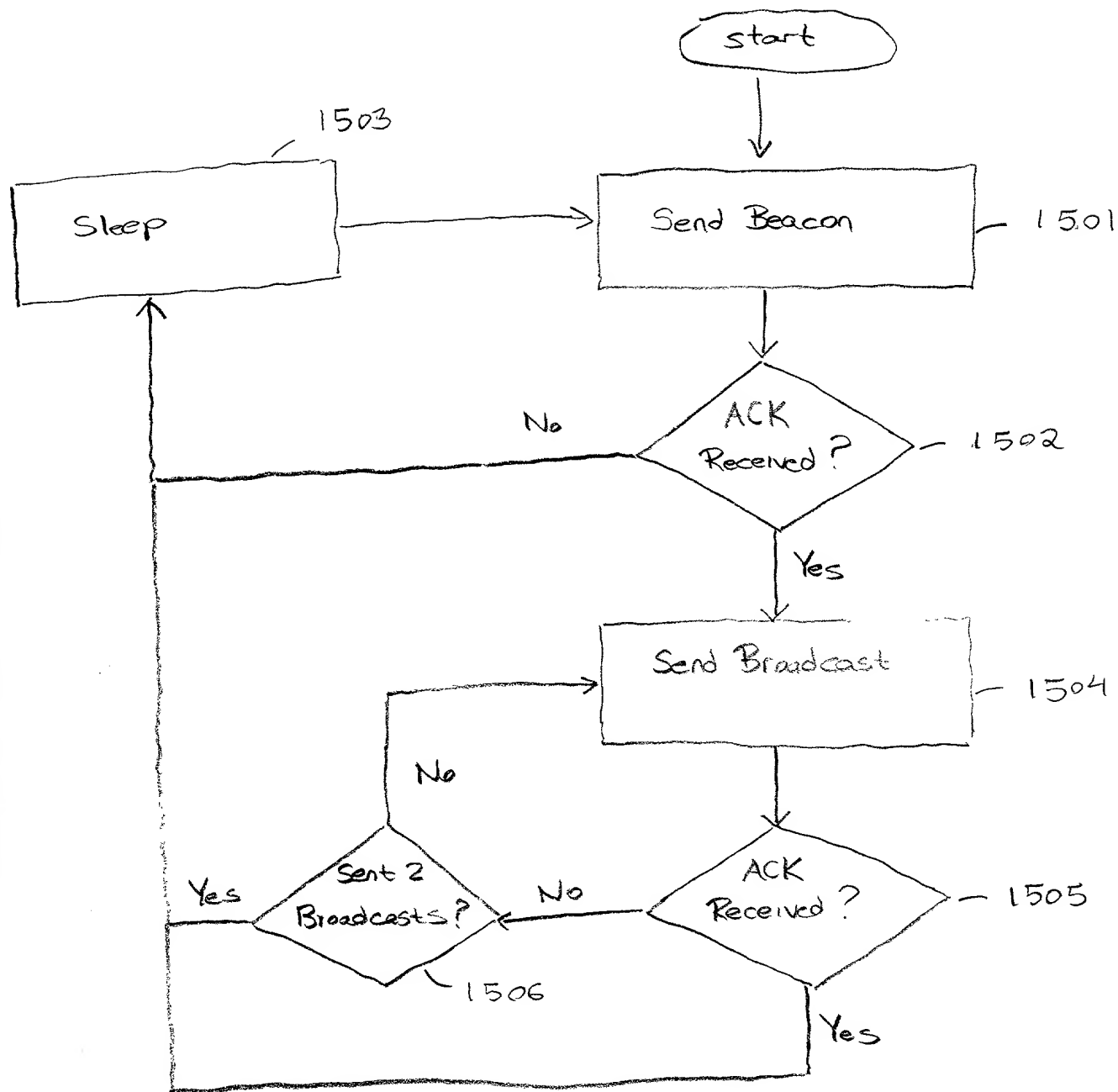


Fig. 15

No ACK Received After Sending Beacon

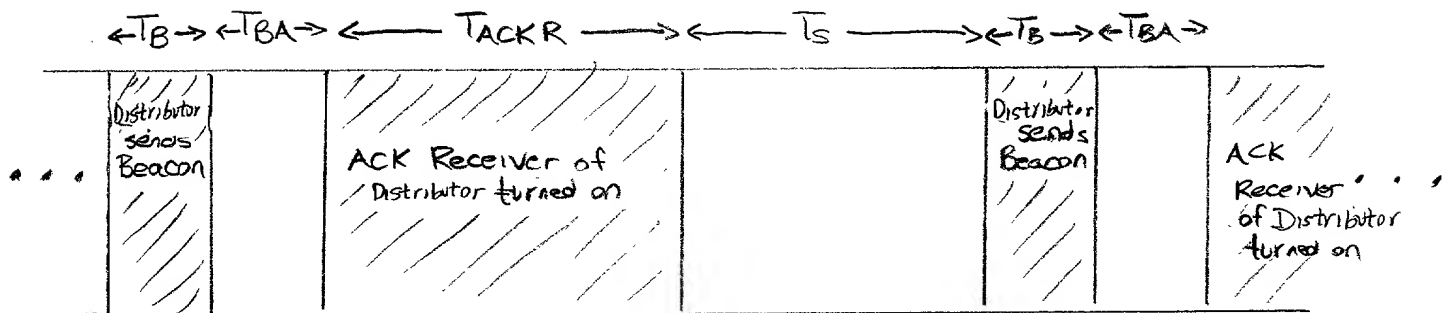


Fig. 16

ACK Received After Sending Beacon

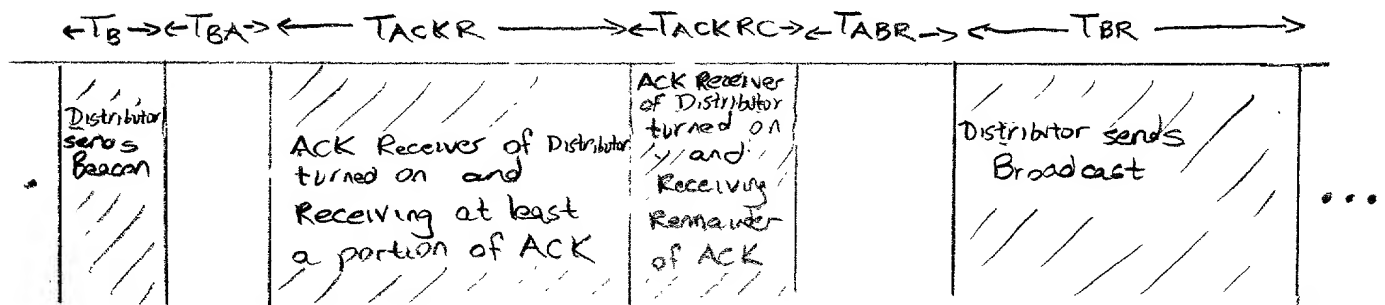


Fig. 17

No ACK Received After Sending Broadcast

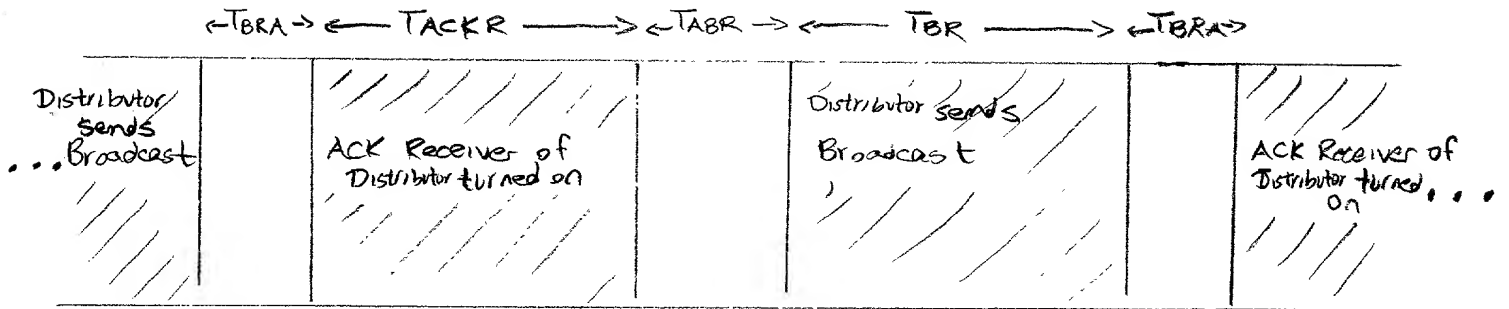


Fig. 18

ACK Received After Sending Broadcast

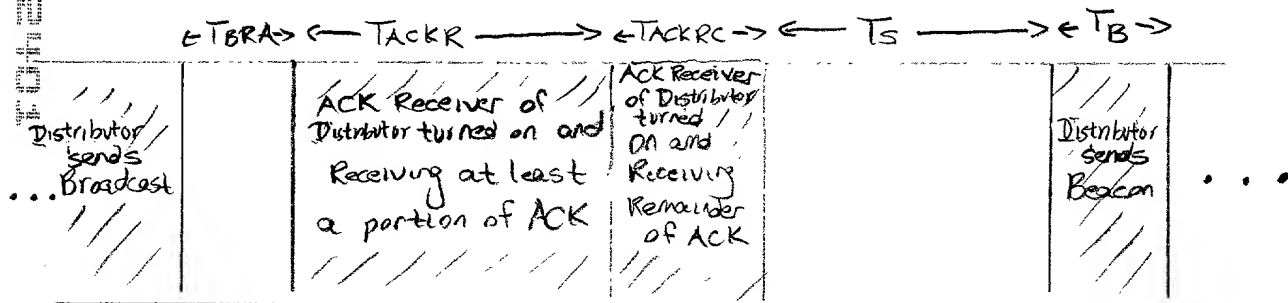


Fig. 19

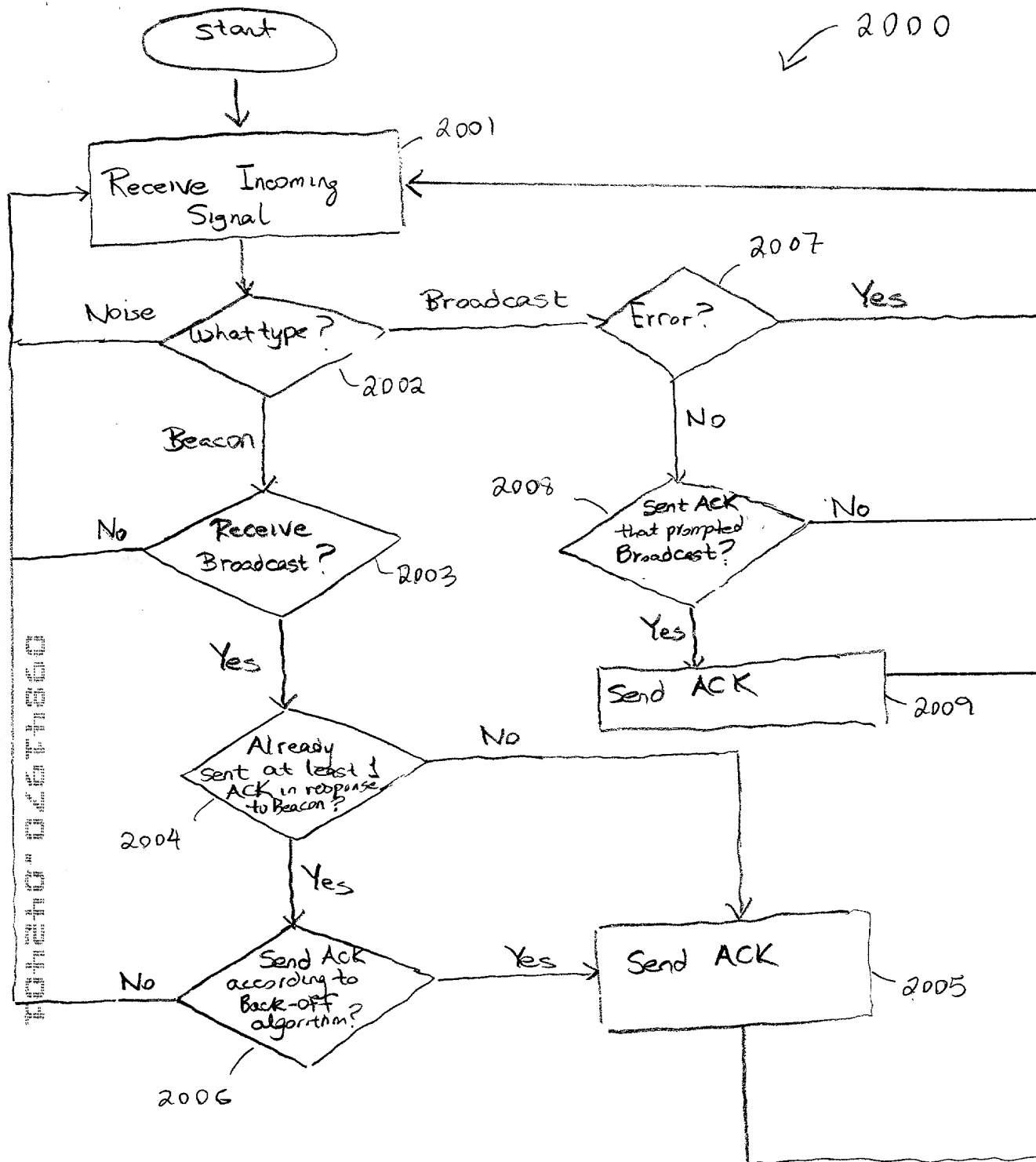


Fig. 20

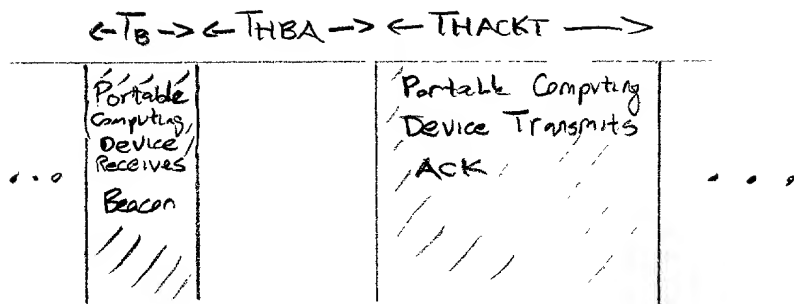


Fig. 21

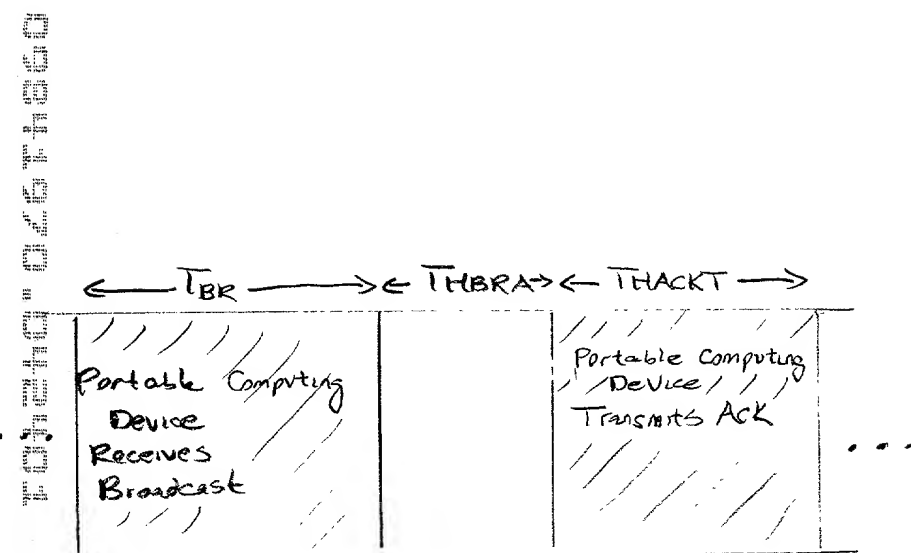


Fig. 22

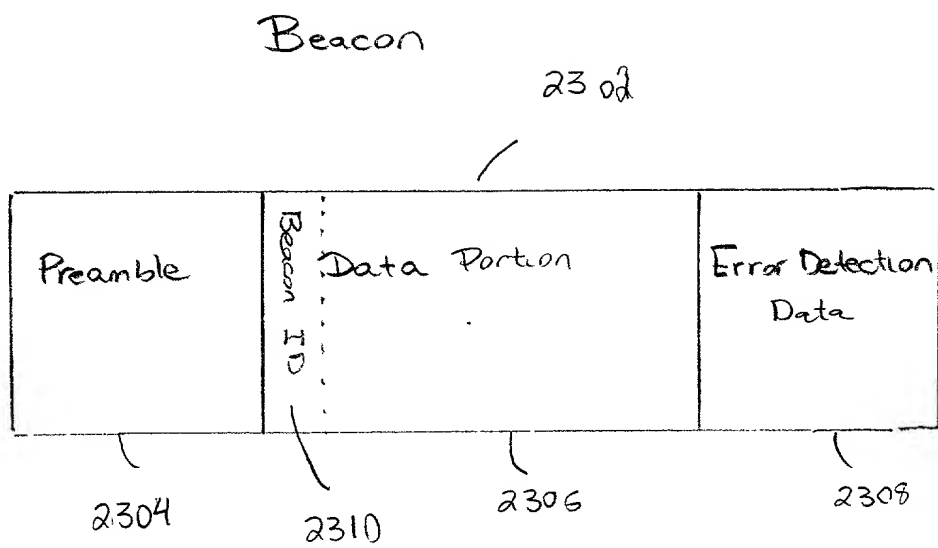


Fig. 23

2025 RELEASE UNDER E.O. 14176

ACK

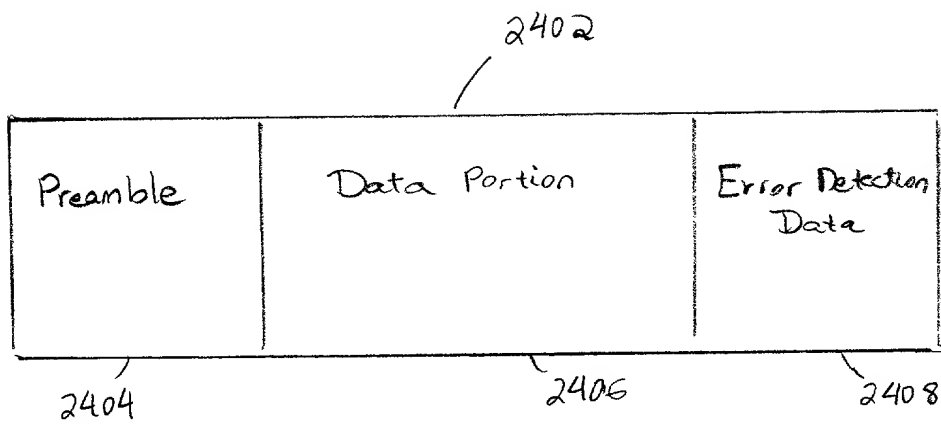


Fig. 24

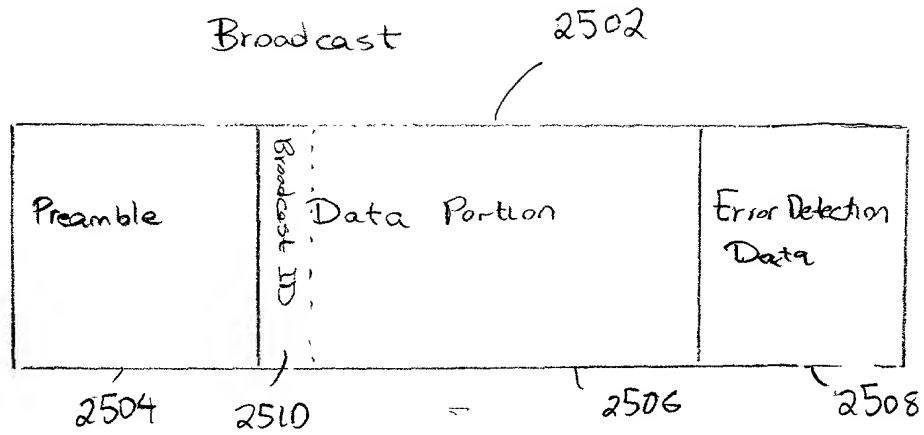


Fig. 25

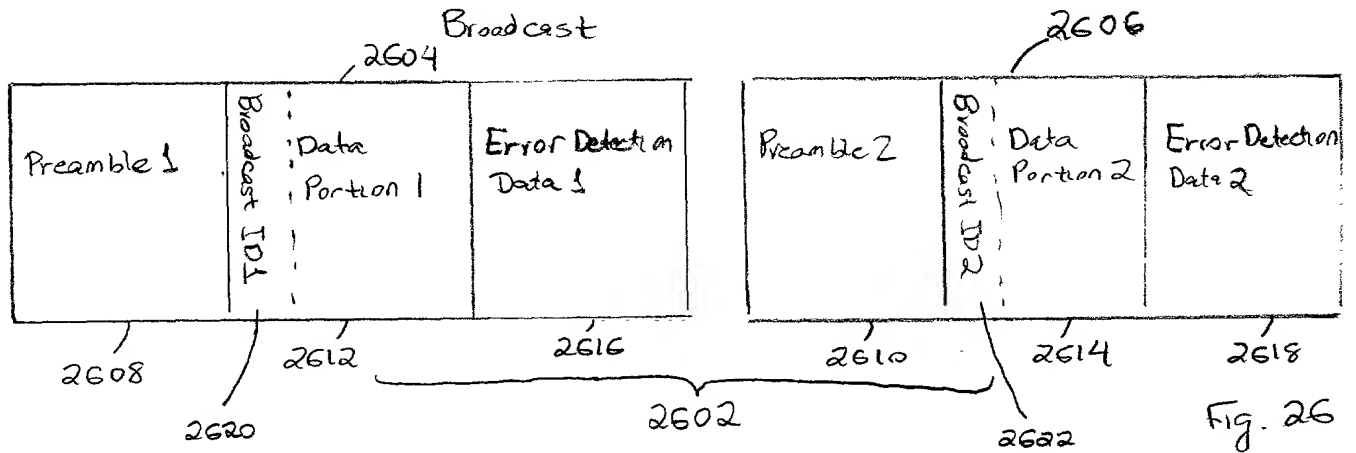


Fig. 26

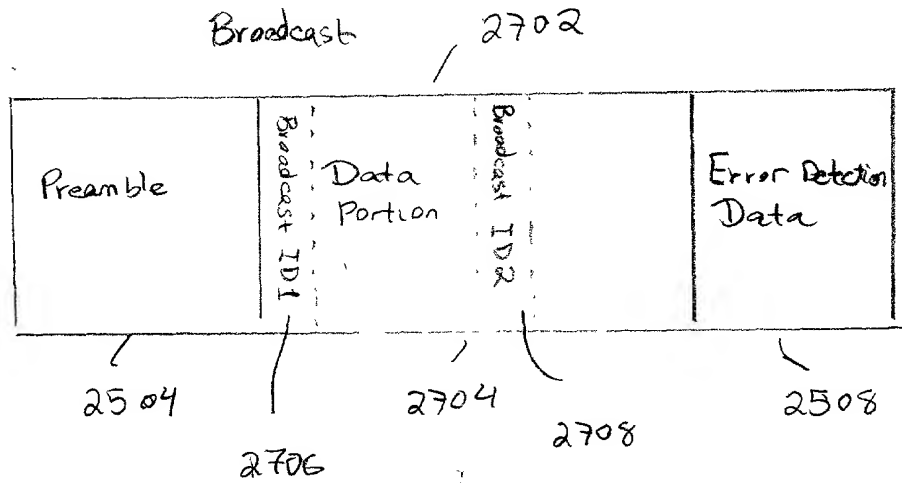


Fig. 27

Battery Conservation

2800

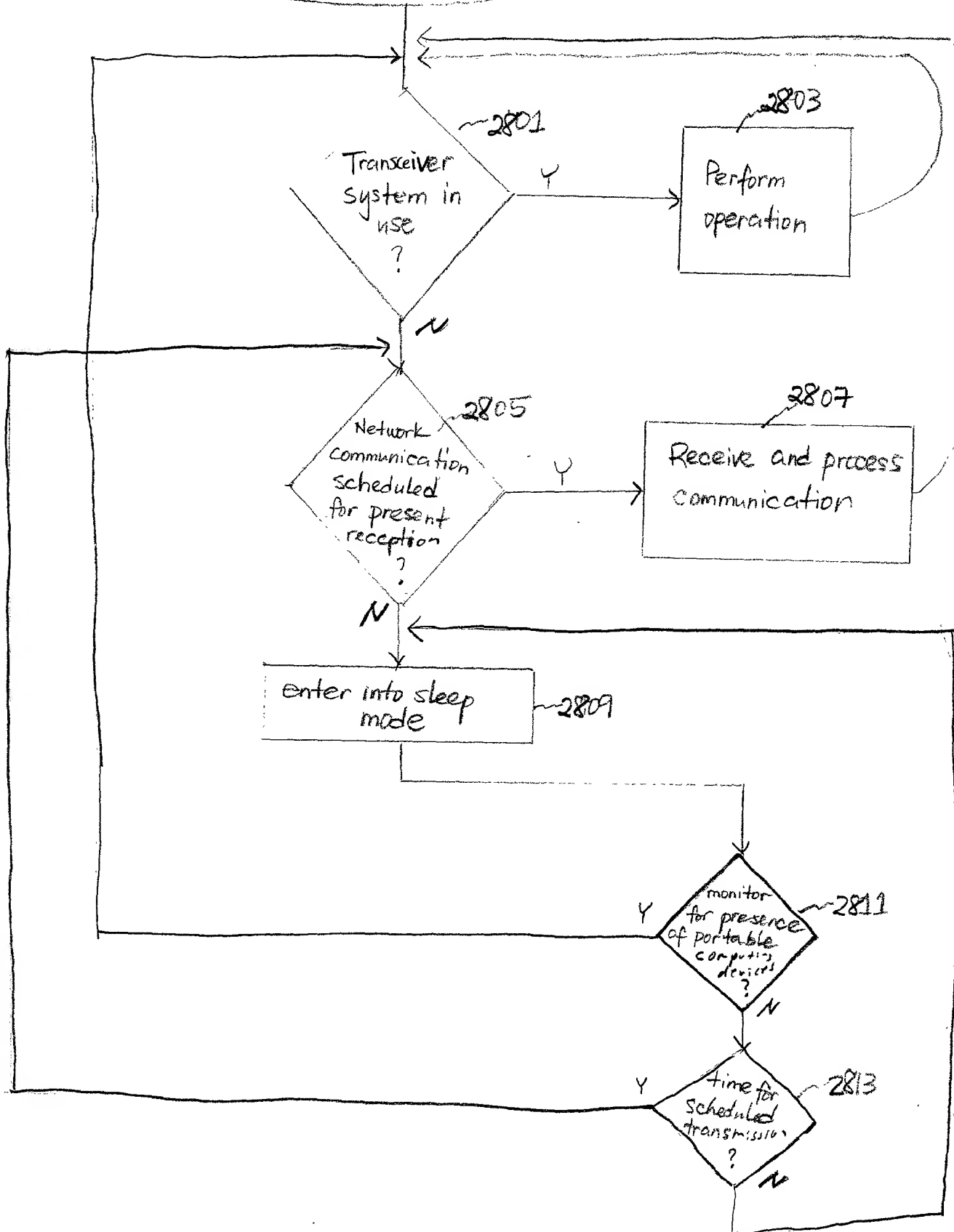


Fig. 28

Patented 06/26/2003

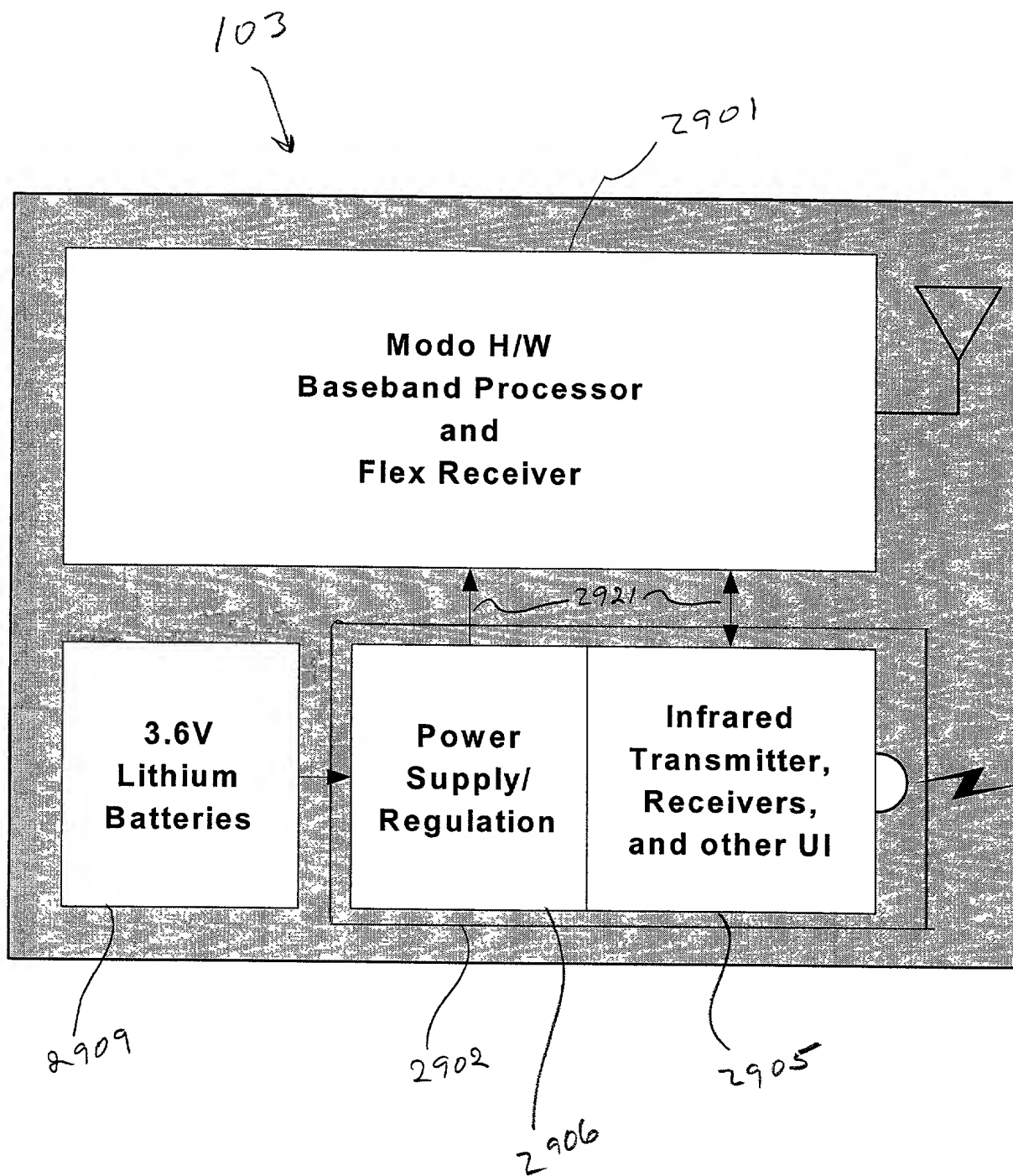


Fig. 29

026400

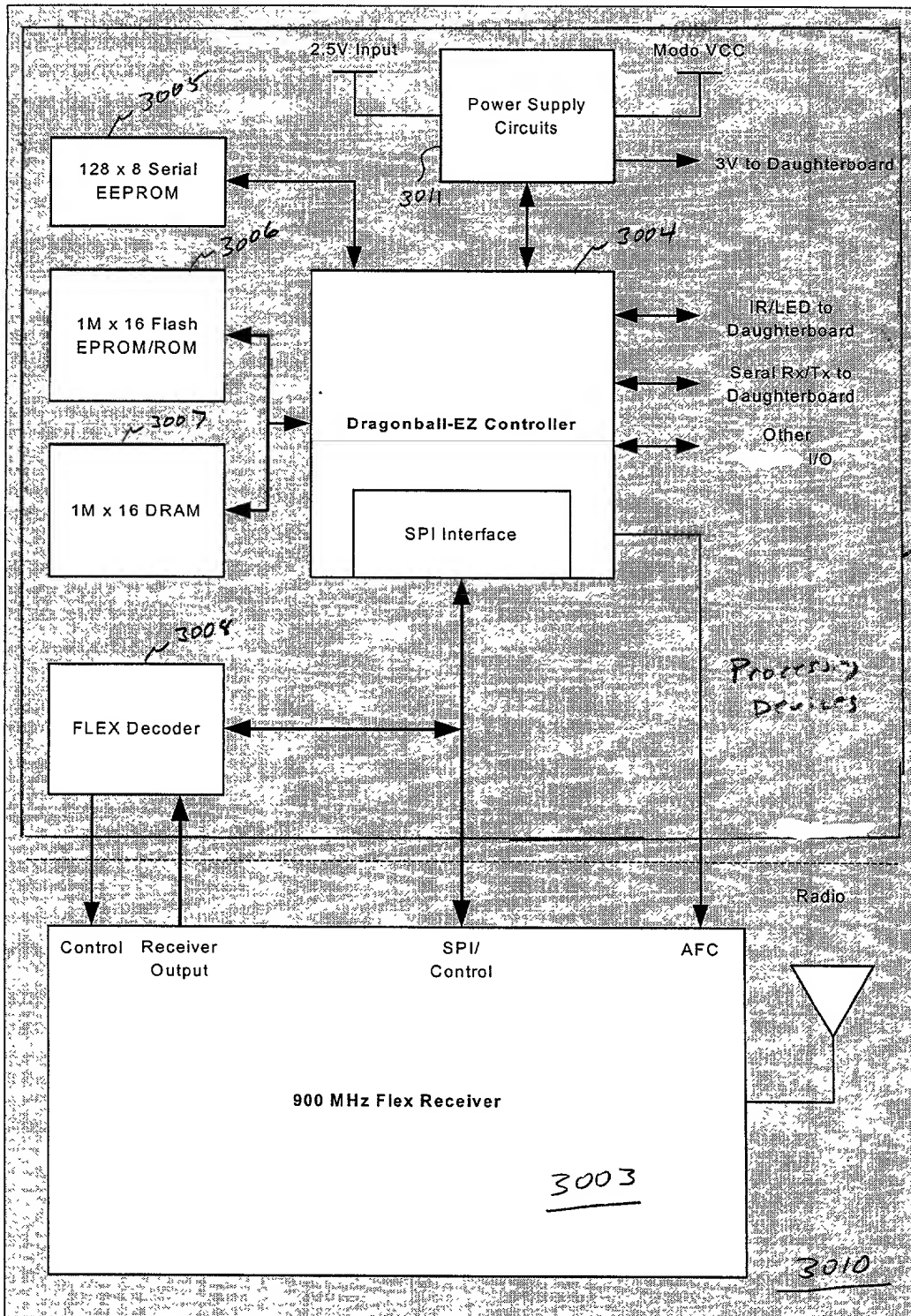
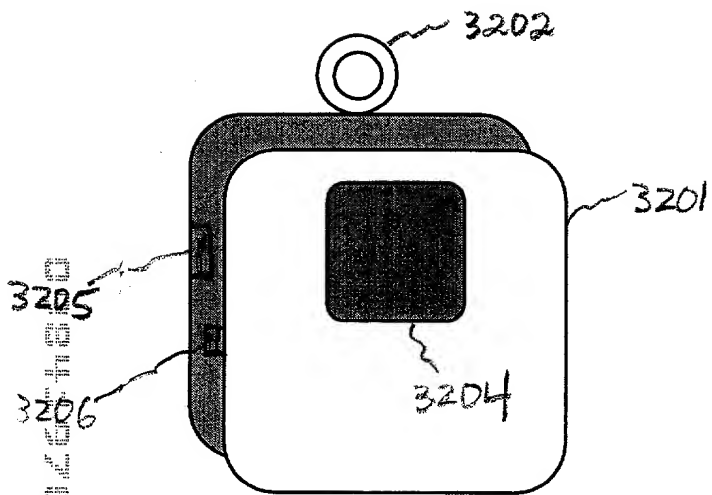


Fig. 30

The diagram, labeled 2902, shows the internal architecture of the RS-232 to IrDA converter. A central **Interface Logic** block (3118) is the core component. It receives three inputs from the **Modo** device: **RS-232 from Modo**, **Control and I/O from Modo**, and **3V from Modo**. The **Interface Logic** is connected to several other components:

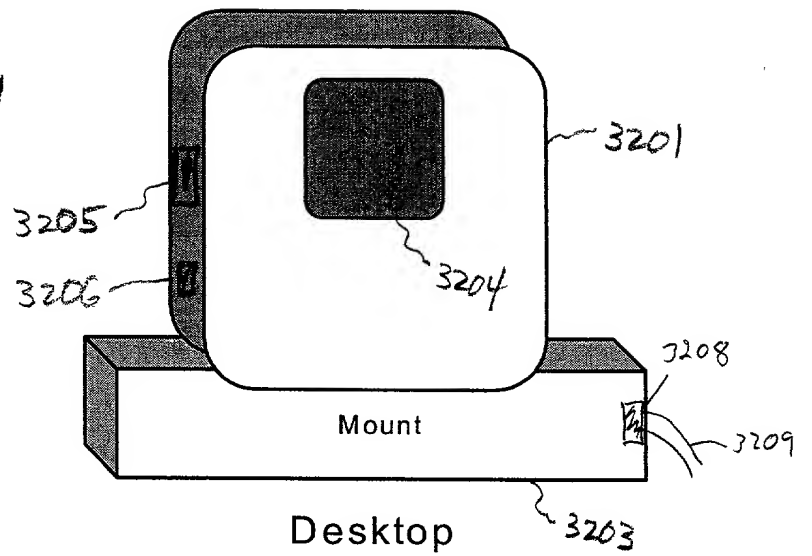
- RS-232 Internal Interface (CMOS)** (3117): Connected via a bidirectional line.
- IrDA Transceiver** (3113): Connected via **Serial Data** and **Power Control** lines.
- High Power IR Transmitters** (3119): Connected via **Serial Data**.
- 38 kHz ASK Receiver** (3115): Connected via **Serial Data** and **Enable** lines.
- Status LED** (3116): Controlled by **LED Control from Modo**.
- Switching Regulator** (3112): Receives **3.6V from lithium battery** and provides **2.5V to Modo**.

Fig. 31



Wall Mount

Fig. 32A



Desktop

Fig. 32B

FIG. 33A

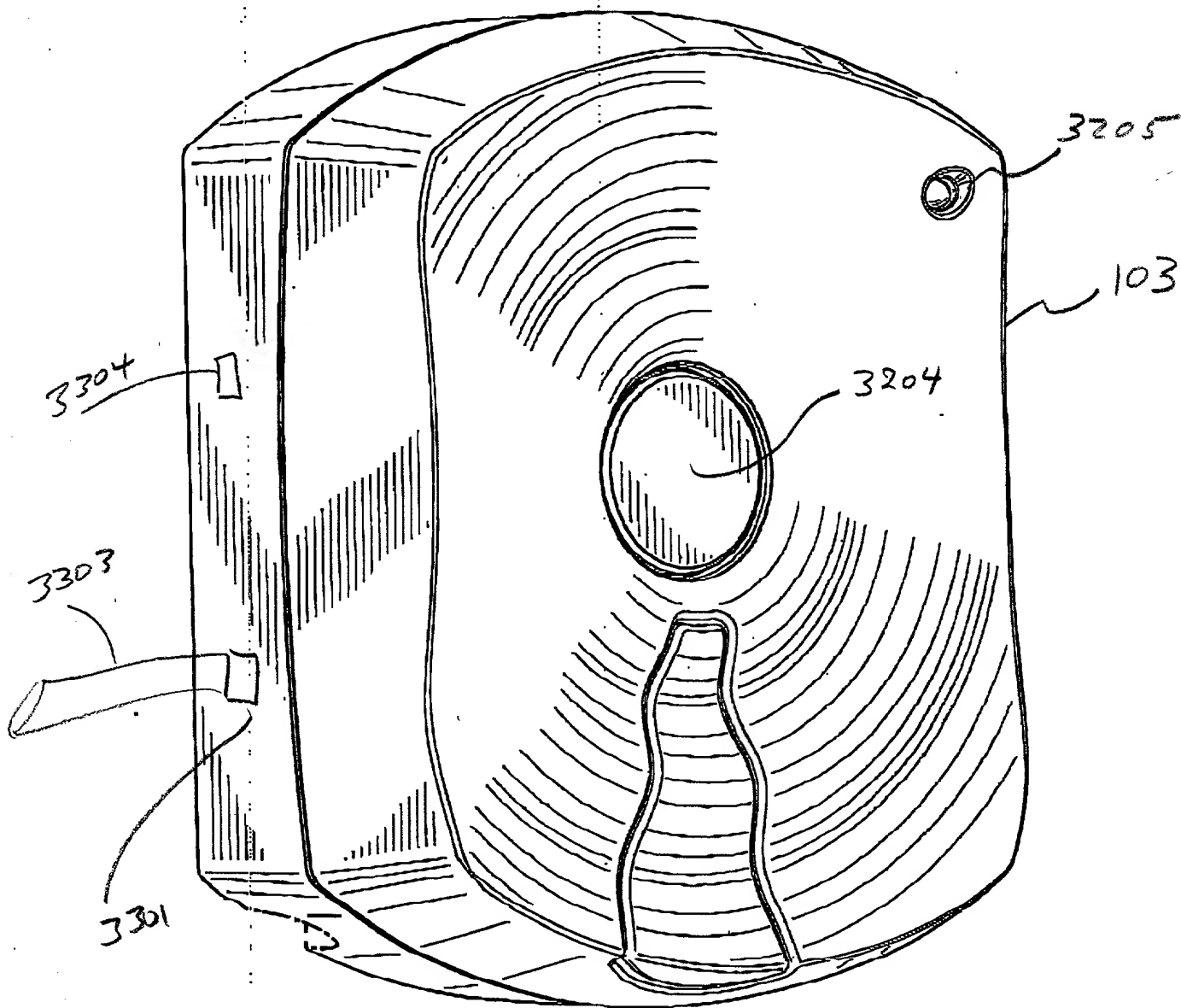


Fig. 33A

LOOKS & TIPS

3305



GREAT FACES

COOL STEEL CREAMY NUDE OPAL SHINE



BRIDAL



BLACK TIE



AFRICAN AMERICAN



LATINA



ASIAN



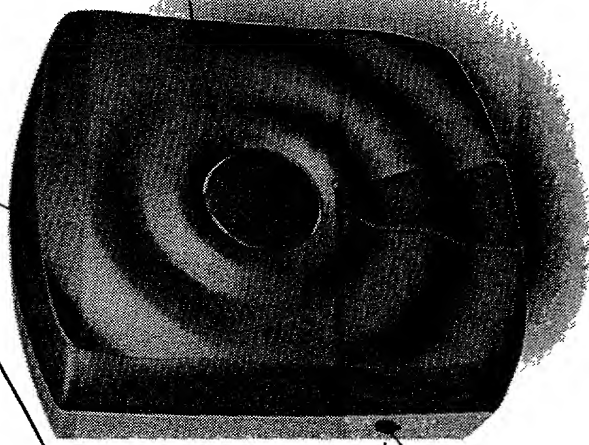
TEEN



AGELESS BEAUTY

"It's not about rules. It's about what works for you."

103



3204

3302

3306

3307

BUY SOME TODAY

F24 33B

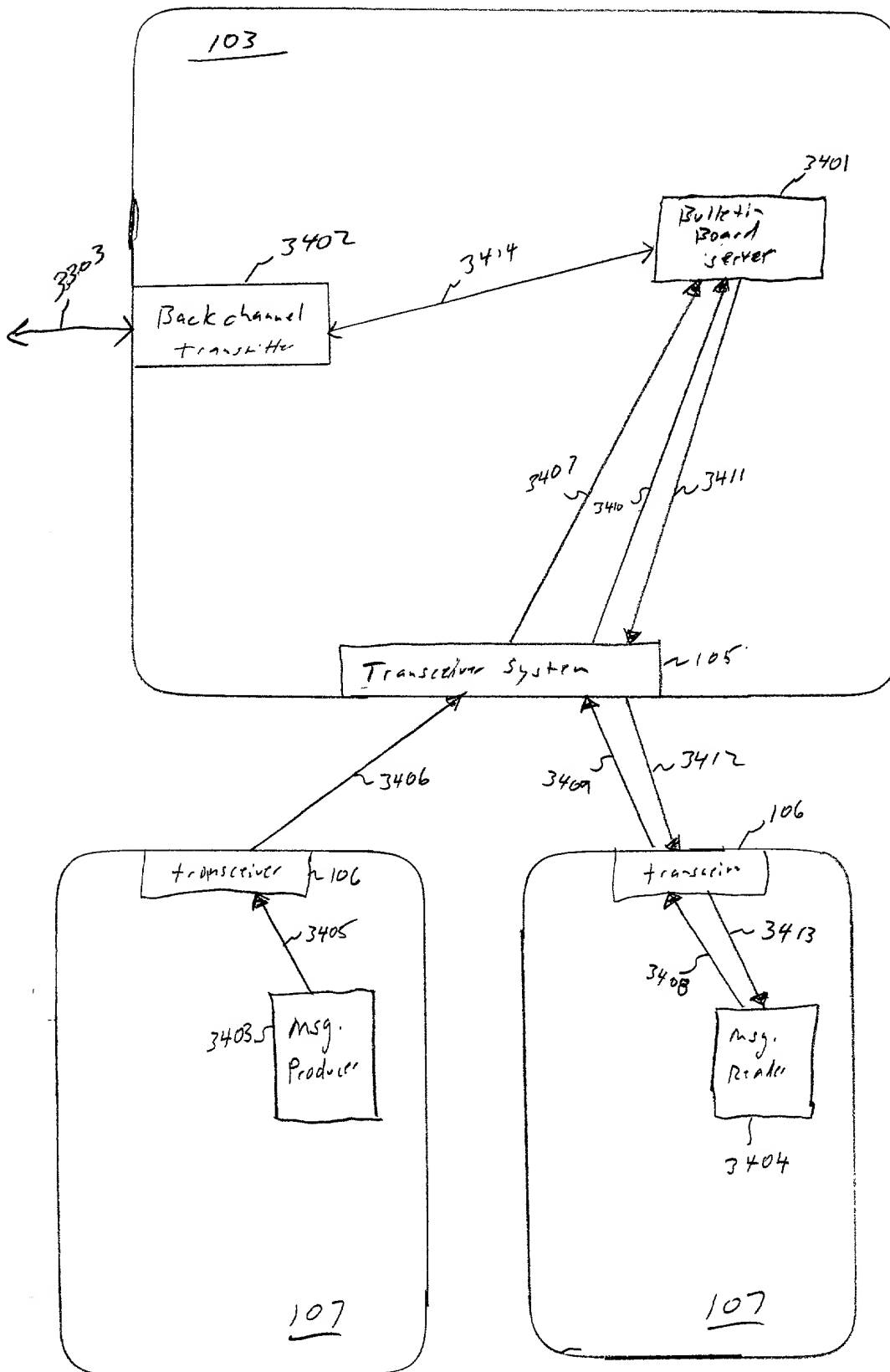


FIG. 34

3500

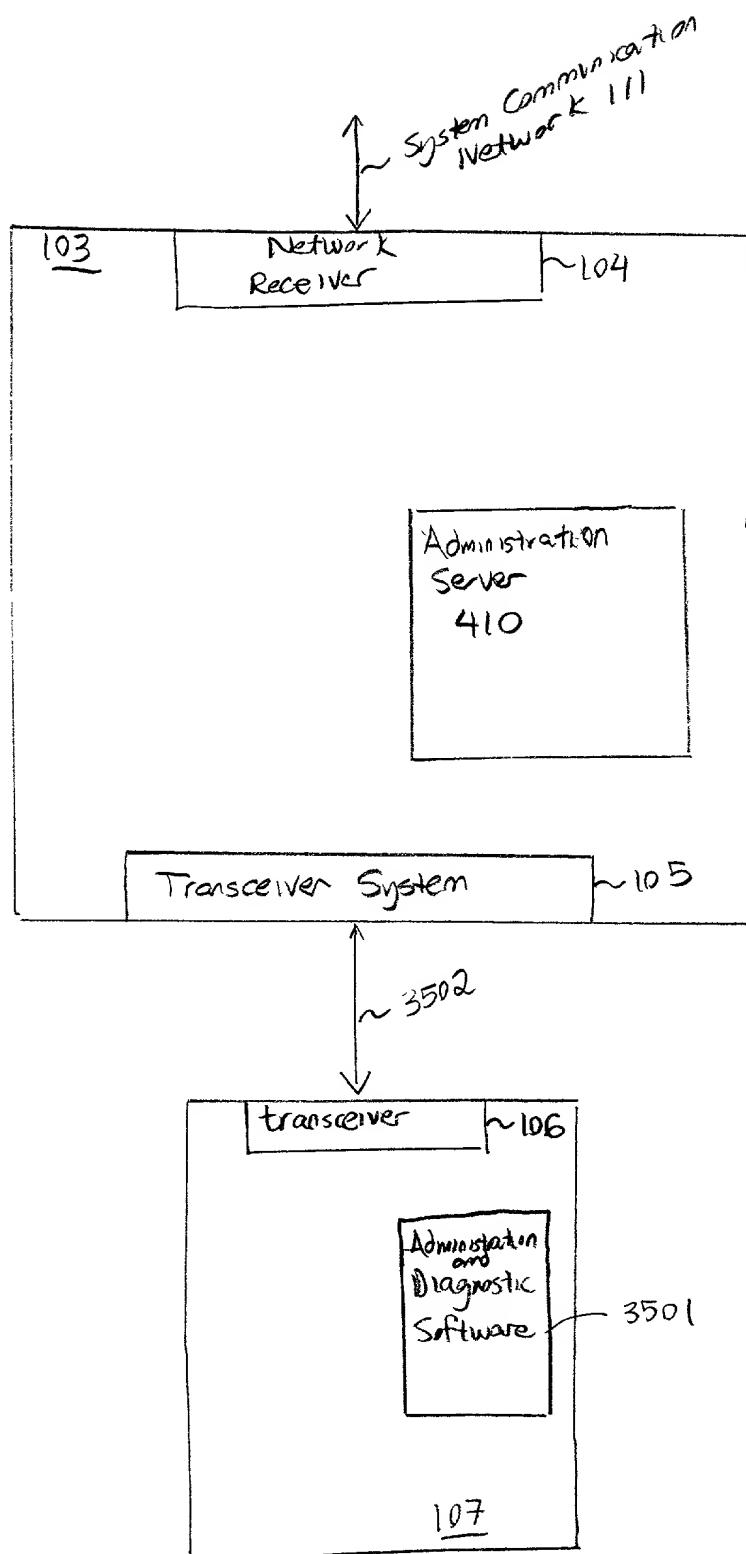


Fig. 35